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Applicants:

Y. Arima et al.

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Title:

INTEGRATED CIRCUIT DEVICE

U.S. PATENT DOCUMENTS

None

FOREIGN PATENT DOCUMENTS

*Examiner Cite			Kind	Publication Date		English Abstract Translation
Initial	No.	Document No.	Code	(mm/dd/yyyy)	Country	Provided Provided
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JPT	_ F11	06-052070	A2	02/25/1994	JP	X
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OTHER INFORMATION (Including Author, Title, Date, Pertinent Pages, Etc.)

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^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.